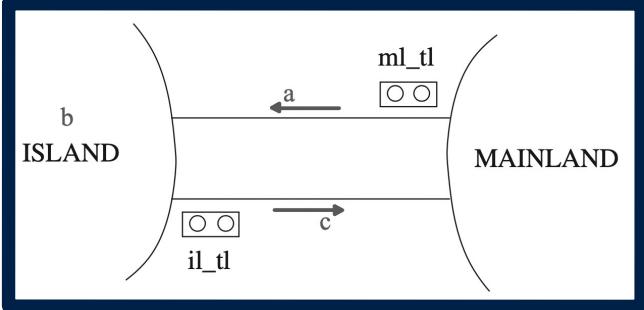


Lecture

Reactive System: Bridge Controller

*2nd Refinement: Fixing the Model
Adding Actions*

Fixing m2: Adding Actions



ML_tl_green

```

when
  ml_tl = red
  a + b < d
  c = 0
then
  ml_tl := green
  il_tl := red
end

```

$ml_tl' = \underline{g}$

IL_tl_green

```

when
  il_tl = red
  b > 0
  a = 0
then
  il_tl := green
  ml_tl := red
end

```

Exercise: Specify IL_tl_green/inv2_5/INV

.
ML_tl_green/inv2_5/INV

axm0_1	$d \in \mathbb{N}$
axm0_2	$d > 0$
axm2_1	$COLOUR = \{green, red\}$
axm2_2	$green \neq red$
inv0_1	$n \in \mathbb{N}$
inv0_2	$n \leq d$
inv1_1	$a \in \mathbb{N}$
inv1_2	$b \in \mathbb{N}$
inv1_3	$c \in \mathbb{N}$
inv1_4	$a + b + c = n$
inv1_5	$a = 0 \vee c = 0$
inv2_1	$ml_tl \in COLOUR$
inv2_2	$il_tl \in COLOUR$
inv2_3	$ml_tl = green \Rightarrow a + b < d \wedge c = 0$
inv2_4	$il_tl = green \Rightarrow b > 0 \wedge a = 0$
inv2_5	$ml_tl = red \vee il_tl = red$



*Concrete
guide*

$\left\{ \begin{array}{l} ml_tl = red \\ a + b < d \\ c = 0 \end{array} \right.$

Exercise: Proof

|

* $green = red \vee red = red$

* $ml_tl' = red \vee il_tl' = red$

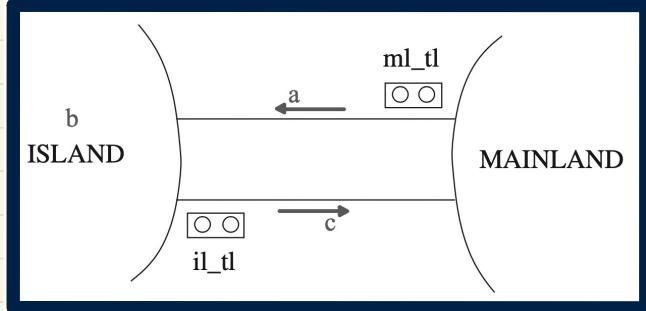
Lecture

Reactive System: Bridge Controller

*2nd Refinement: Fixing the Model
Splitting Events*

Invariant Preservation: ML_out/inv2_3/INV

↓ ML_out/inv2_4 discussed earlier



variables:
 a, b, c
 ml_tl
 il_tl

ML_out
when
 $ml_tl = \text{green}$
then
 $a := a + 1$
end

IL_out
when
 $il_tl = \text{green}$
then
 $b := b - 1$
 $c := c + 1$
end

invariants:

inv2_1 : $ml_tl \in \text{COLOUR}$
 inv2_2 : $il_tl \in \text{COLOUR}$
 inv2_3 : $ml_tl = \text{green} \Rightarrow a + b < d \wedge c = 0$
 inv2_4 : $il_tl = \text{green} \Rightarrow b > 0 \wedge a = 0$

ML_out/inv2_3/INV



Concrete guards of **ML_out**

Concrete invariant **inv2_3**
 with **ML_out**'s effect in the post-state

$d \in \mathbb{N}$
 $d > 0$
 $\text{COLOUR} = \{\text{green}, \text{red}\}$
 $\text{green} \neq \text{red}$
 $n \in \mathbb{N}$
 $n \leq d$
 $a \in \mathbb{N}$
 $b \in \mathbb{N}$
 $c \in \mathbb{N}$
 $a + b + c = n$
 $a = 0 \vee c = 0$
 $ml_tl \in \text{COLOUR}$
 $il_tl \in \text{COLOUR}$
 $ml_tl = \text{green} \Rightarrow a + b < d \wedge c = 0$
 $il_tl = \text{green} \Rightarrow b > 0 \wedge a = 0$
 $ml_tl = \text{red} \vee il_tl = \text{red}$
 $ml_tl = \text{green}$

↳

$\{ ml_tl = \text{green} \Rightarrow (a + 1) + b < d \wedge c = 0 \}$

→ IL_out/inv2_3
discussed earlier

Exercise: Specify IL_out/inv2_4/INV

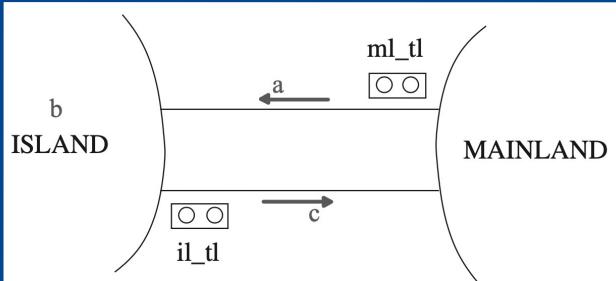
Discharging POs of m2: Invariant Preservation

First Attempt

$d \in \mathbb{N}$
 $d > 0$
 $\text{COLOUR} = \{\text{green}, \text{red}\}$
 $\text{green} \neq \text{red}$
 $n \in \mathbb{N}$
 $n \leq d$
 $a \in \mathbb{N}$
 $b \in \mathbb{N}$
 $c \in \mathbb{N}$
 $a + b + c = n$
 $a = 0 \vee c = 0$
 $\text{ml_tl} \in \text{COLOUR}$
 $\text{il_tl} \in \text{COLOUR}$
 $\text{ml_tl} = \text{green} \Rightarrow a + b < d \wedge c = 0$
 $\text{il_tl} = \text{green} \Rightarrow b > 0 \wedge a = 0$
 $\text{ml_tl} = \text{red} \vee \text{il_tl} = \text{red}$
 $\text{ml_tl} = \text{green}$
 \vdash
 $\text{ml_tl} = \text{green} \Rightarrow (a + 1) + b < d \wedge c = 0$

MON

ML_out/inv2_3/INV



Error

IL_out/
inv2_4/
INV

↳
expected to
see:

a sturdy
unprovable
segment

$$\frac{H \vdash P \quad H \vdash Q}{H \vdash P \wedge Q} \text{ AND_R}$$

$$\frac{H, P, Q \vdash R}{H, P \wedge Q \vdash R} \text{ AND_L}$$

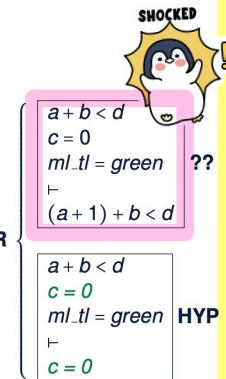
$$\frac{H, P \vdash Q}{H \vdash P \Rightarrow Q} \text{ IMP_R}$$

$$\frac{\vdash \text{ml_tl} = \text{green} \Rightarrow a + b < d \wedge c = 0}{\vdash \text{ml_tl} = \text{green} \Rightarrow (a + 1) + b < d \wedge c = 0}$$

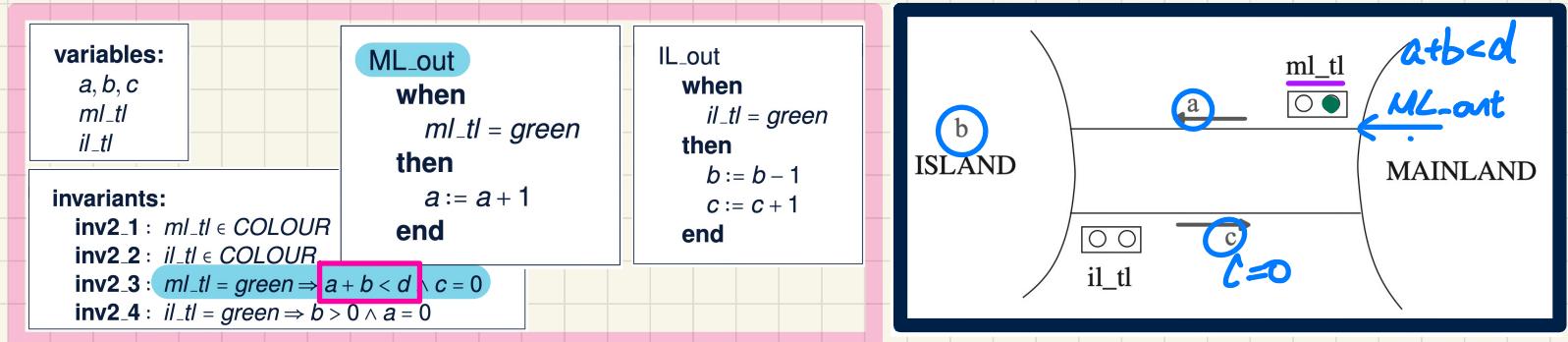
$$\frac{\begin{array}{l} \text{ml_tl} = \text{green} \Rightarrow a + b < d \wedge c = 0 \\ \text{ml_tl} = \text{green} \end{array} \checkmark}{\vdash (a + 1) + b < d \wedge c = 0}$$

$$\frac{\begin{array}{l} a + b < d \wedge c = 0 \\ \vdash \text{ml_tl} = \text{green} \end{array}}{\vdash (a + 1) + b < d \wedge c = 0}$$

$$\frac{\begin{array}{l} a + b < d \\ c = 0 \\ \vdash \text{ml_tl} = \text{green} \end{array}}{\vdash (a + 1) + b < d \wedge c = 0}$$



Understanding the Failed Proof on INV



Unprovable Sequent from ML_out/inv2_3/INV

$$\begin{array}{c}
\underline{a + b < d} \\
\wedge \underline{c = 0} \\
\wedge \checkmark ml_tl = green \\
\vdash \\
(a + 1) + b < d
\end{array}$$



$d = 3, b = 0, a = 0$
$d = 3, b = 1, a = 0$
$d = 3, b = 0, a = 1$
$d = 3, b = 0, a = 2$
$d = 3, b = 1, a = 1$
$d = 3, b = 2, a = 0$

$$(a+1)+b \neq d$$

$$(a+1)+b = d$$

no more ML_out allowed $\Rightarrow ml_tl := red$

$$\begin{array}{l}
x < y \\
\Rightarrow x + 1 < y
\end{array}$$

e.g. $x = 3$
 $y = 4$

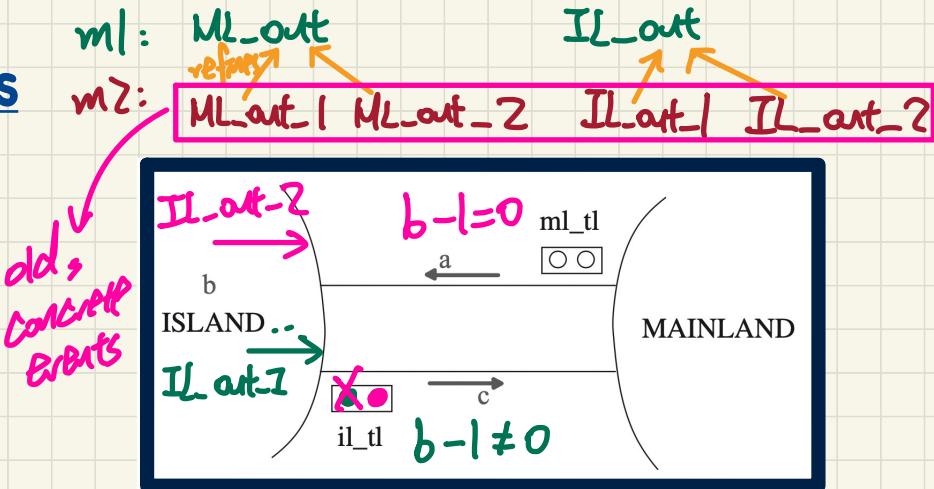
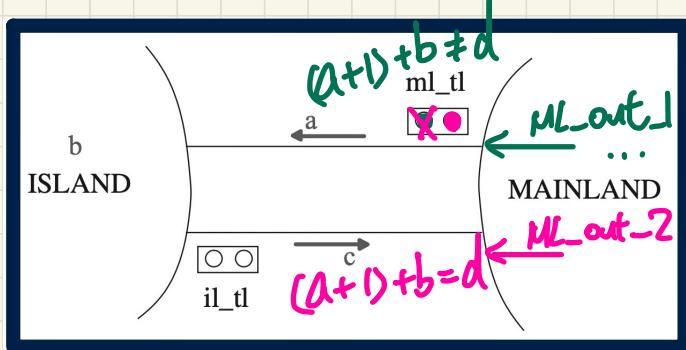
Another
 allowed
 ML_out

! false $\Rightarrow \neg \sqrt{x}$

inv2_3 is proved

$(a+1) + b < d$ evaluates to true
 $((a+1) + b < d$ evaluates to true
 $((a+1) + b < d$ evaluates to true
 $((a+1) + b < d$ evaluates to false
 $((a+1) + b < d$ evaluates to false
 $((a+1) + b < d$ evaluates to false
 $((a+1) + b < d$ evaluates to false

Fixing m2: Splitting Events



```
ML_out_1
when
  ml_tl = green
  a + b + 1 ≠ d
then
  a := a + 1
end
```

```
ML_out_2
when
  ml_tl = green
  a + b + 1 = d
then
  a := a + 1
  ml_tl := red
end
```



```
IL_out_1
when
  il_tl = green
  b ≠ 1
then
  b := b - 1
  c := c + 1
end
```

```
IL_out_2
when
  il_tl = green
  b = 1
then
  b := b - 1
  c := c + 1
  il_tl := red
end
```

6 ↑ 8

ML-out split
IL-out split

of segments for Inv:

$$8 \times 5 = 40$$

Lecture

Reactive System: Bridge Controller

2nd Refinement: Livelock/Divergence

Current m2 May Livelock

```

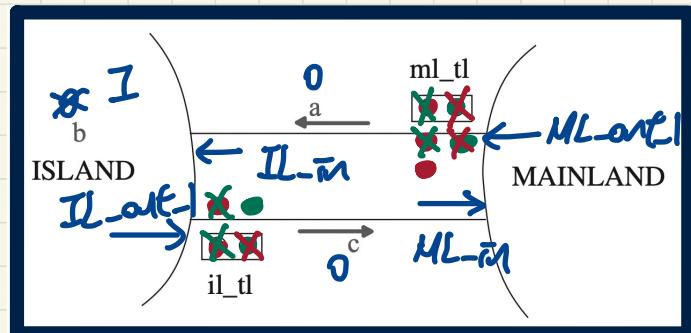
ML_tl_green
when
✓ ml_tl = red
✓ a + b < d
✓ c = 0
then
    ml_tl := green
    il_tl := red
end

```

```

IL_tl_green
when
il_tl = red
b > 0
a = 0
then
    il_tl := green
    ml_tl := red
end

```



Expected trace: no divergent transitions

d=2

<init, ML-tl-green, ML-out_1, IL-tl, IL-out_1>

a new event

Is ML-tl green enabled?

Is IL-tl green enabled?

(<u>init</u>	,	<u>ML-tl_green</u>	,	<u>ML_out_1</u>	,	<u>IL_in</u>	,	<u>IL-tl.green</u>	,	<u>ML-tl_green</u>	,	<u>IL-tl_green</u>	, ...)	
	$d = 2$		$d = 2$		$d = 2$		$d = 2$		$d = 2$		$d = 2$		$d = 2$		
	$a' = 0$		$a' = 0$		$a' = 1$		$a' = 0$								
	$b' = 0$		$b' = 0$		$b' = 0$		$b' = 1$								
	$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		
	<i>ml_tl = red</i>		<i>ml_tl' = green</i>		<i>ml_tl' = green</i>		<i>ml_tl' = green</i>		<i>ml_tl' = red</i>		<i>ml_tl' = red</i>		<i>ml_tl' = red</i>		
	<i>il_tl = red</i>		<i>il_tl' = red</i>		<i>il_tl' = red</i>		<i>il_tl' = red</i>		<i>il_tl' = green</i>		<i>il_tl' = green</i>		<i>il_tl' = green</i>		

<i>ml_tl' = red</i>	<i>ml_tl' = green</i>	<i>ml_tl' = red</i>
<i>il_tl' = green</i>	<i>il_tl' = red</i>	<i>il_tl' = green</i>

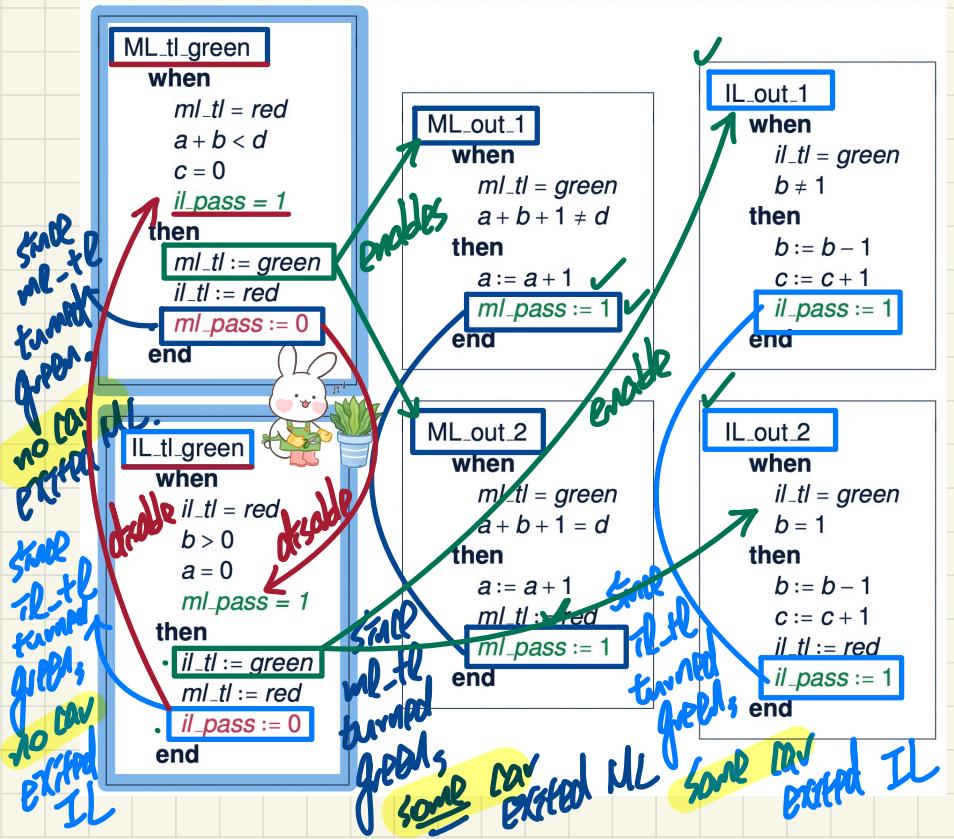


pattern of divergence

Fixing m2: Regulating Traffic Light Changes

To break the divergence patterns
after both view part
occurring, some old events clear.

Divergence Trace: <init, ML_tl_green, ML_out_1, IL_in, IL_tl_green, ML_tl_green, ML_out_1, IL_in, ...>



$d = 2$	ml_pass	il_pass
< init,	1	1
ML_tl_green,	0	1
ML_out_1,	1	1
ML_out_2,	1	1
IL_in,	1	1
IL_in,	1	1
IL_tl_green,	1	0
IL_out_1,	1	1
IL_out_2,	1	1
ML_in,	1	1
ML_in	1	1

Fixing m2: Measuring Traffic Light Changes

```

ML_tl_green
when
  ml_tl = red
  a + b < d
  c = 0
  il_pass = 1
then
  ml_tl := green
  il_tl := red
  ml_pass := 0
end

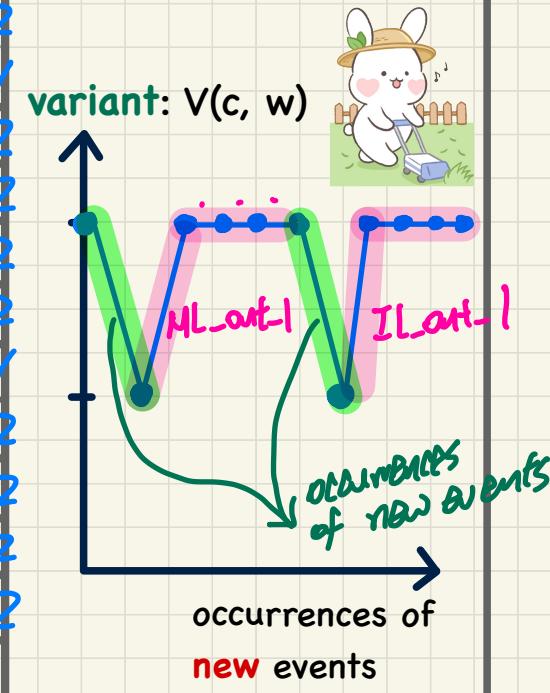
```

```

IL_tl_green
when
  il_tl = red
  b > 0
  a = 0
  ml_pass = 1
then
  il_tl := green
  ml_tl := red
  il_pass := 0
end

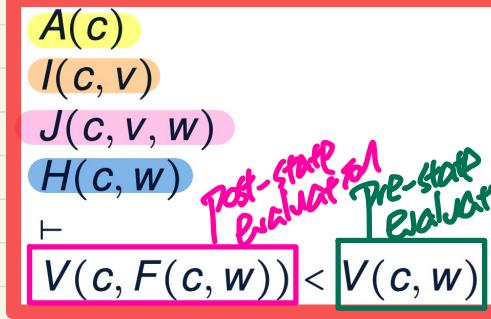
```

$d = 2$	ml_pass	il_pass	variants: $ml_pass + il_pass$
< init,	1	1	2
ML_tl_green,	0	1	1
ML_out_1,	1	1	2
ML_out_2,	1	1 dd	2
IL_in,	1	1 Bob	2
IL_in,	1	1	2
IL_tl_green,	1	0	1
IL_out_1,	1	1	2
IL_out_2,	1	1 dd	2
ML_in,	1	1 BobS	2
ML_in	1	1	2
>			



PO of Convergence/Non-Divergence/Livelock Freedom

A New Event Occurrence Decreases Variant



VAR
 applicable
 to new
 events

Variants: $ml_pass + il_pass$

$$* \frac{0}{ml_pass + il_pass} < \frac{TL_pass}{ml_pass + TL_pass}$$

ML_tl_green/VAR

```

ML_tl_green
when
  ml_tl = red
  a + b < d
  c = 0
  il_pass = 1
then
  ml_tl := green
  il_tl := red
  ml_pass := 0
end
  
```

BIP:
~~ml_pass = 0~~
~~il_pass = 1~~
~~tl_pass = 0~~
~~tl_pass = 1~~
~~tl_pass = 0~~



$d \in \mathbb{N}$	$d > 0$
$COLOUR = \{green, red\}$	$green \neq red$
$n \in \mathbb{N}$	$n \leq d$
$a \in \mathbb{N}$	$b \in \mathbb{N}$
$a + b + c = n$	$a = 0 \vee c = 0$
$ml_tl \in COLOUR$	$il_tl \in COLOUR$
$ml_tl = green \Rightarrow a + b < d \wedge c = 0$	$il_tl = green \Rightarrow b > 0 \wedge a = 0$
$ml_tl = red \vee il_tl = red$	
$ml_pass \in \{0, 1\}$	$il_pass \in \{0, 1\}$
$ml_tl = red \Rightarrow ml_pass = 1$	$il_tl = red \Rightarrow il_pass = 1$
$ml_tl = red$	$a + b < d$
$il_pass = 1$	

$$0 + il_pass < ml_pass + il_pass$$

Concrete guards of
ML_tl_green

Lecture

Reactive System: Bridge Controller

*2nd Refinement:
Relative Deadlock Freedom*

PO of Relative Deadlock Freedom

axm0.1 $\{ d \in \mathbb{N} \}$
 axm0.2 $\{ d > 0 \}$
 axm2.1 $\{ COLOUR = \{green, red\} \}$
 axm2.2 $\{ green \neq red \}$
 inv0.1 $\{ n \in \mathbb{N} \}$
 inv0.2 $\{ n \leq d \}$
 inv1.1 $\{ a \in \mathbb{N} \}$
 inv1.2 $\{ b \in \mathbb{N} \}$
 inv1.3 $\{ c \in \mathbb{N} \}$
 inv1.4 $\{ a + b + c = n \}$
 inv1.5 $\{ a = 0 \vee c = 0 \}$
 inv2.1 $\{ ml_tl \in COLOUR \}$
 inv2.2 $\{ il_tl \in COLOUR \}$
 inv2.3 $\{ ml_tl = green \Rightarrow a + b < d \wedge c = 0 \}$
 inv2.4 $\{ il_tl = green \Rightarrow b > 0 \wedge a = 0 \}$
 inv2.5 $\{ ml_tl = red \vee il_tl = red \}$
 inv2.6 $\{ ml_pass \in \{0, 1\} \}$
 inv2.7 $\{ il_pass \in \{0, 1\} \}$
 inv2.8 $\{ ml_tl = red \Rightarrow ml_pass = 1 \}$
 inv2.9 $\{ il_tl = red \Rightarrow il_pass = 1 \}$
 $\quad \quad \quad \{ a + b < d \wedge c = 0 \}$
 $\quad \quad \quad \vee \quad \quad \quad \{ c > 0 \}$
 $\quad \quad \quad \vee \quad \quad \quad \{ a > 0 \}$
 $\quad \quad \quad \vee \quad \quad \quad \{ b > 0 \wedge a = 0 \}$

Disjunction of *abstract* guards



Disjunction of *concrete* guards

Abstract m1

variables: a, b, c

invariants:

- inv1.1 : $a \in \mathbb{N}$
- inv1.2 : $b \in \mathbb{N}$
- inv1.3 : $c \in \mathbb{N}$
- inv1.4 : $a + b + c = n$
- inv1.5 : $a = 0 \vee c = 0$

ML_out

```
when
  a + b < d
  c = 0
then
  a := a + 1
end
```

ML_in

```
when
  c > 0
then
  c := c - 1
end
```

IL_in

```
when
  a > 0
then
  a := a - 1
  b := b + 1
end
```

IL_out

```
when
  b > 0
  a = 0
then
  b := b - 1
  c := c + 1
end
```

Concrete m2

ML_tl.green

```
when
  ml\_tl = red
  b > 0
  a = 0
  ml\_pass = 1
then
  ml\_tl := green
  ml\_tl := red
  ml\_pass := 0
end
```

IL_tl_green

```
when
  ml\_tl = green
  a + b + 1 ≠ d
then
  a := a + 1
  ml\_tl := 1
end
```

ML_out_1

```
when
  ml\_tl = green
  a + b + 1 ≠ d
then
  a := a + 1
  ml\_pass := 1
end
```

IL_out_1

```
when
  il\_tl = green
  b ≠ 1
then
  b := b - 1
  c := c + 1
  il\_pass := 1
end
```

guards of *ML_out* in *m₁*
 guards of *ML_in* in *m₁*
 guards of *IL_in* in *m₁*
 guards of *IL_out* in *m₁*

guards of *ML_tl.green* in *m₂*
 guards of *IL_tl.green* in *m₂*
 guards of *ML_out_1* in *m₂*
 guards of *ML_out_2* in *m₂*
 guards of *ML_in* in *m₂*
 guards of *IL_in* in *m₂*

guards of *ML_out* in *m₁*
 guards of *ML_in* in *m₁*
 guards of *IL_in* in *m₁*
 guards of *IL_out* in *m₁*
 guards of *ML_out* in *m₂*
 guards of *ML_in* in *m₂*
 guards of *IL_in* in *m₂*

IL_in

```
when
  a > 0
then
  a := a - 1
  b := b + 1
end
```

ML_in

```
when
  c > 0
then
  c := c - 1
end
```

Discharging POs of m2: Relative Deadlock Freedom

```

 $d \in \mathbb{N}$ 
 $d > 0$ 
 $\text{COLOUR} = \{\text{green}, \text{red}\}$ 
 $\text{green} \neq \text{red}$ 
 $n \in \mathbb{N}$ 
 $n \leq d$ 
 $a \in \mathbb{N}$ 
 $b \in \mathbb{N}$ 
 $c \in \mathbb{N}$ 
 $a + b + c = n$ 
 $a = 0 \vee c = 0$ 
 $ml\_tl \in \text{COLOUR}$ 
 $il\_tl \in \text{COLOUR}$ 
 $ml\_tl = \text{green} \Rightarrow a + b < d \wedge c = 0$ 
 $il\_tl = \text{green} \Rightarrow b > 0 \wedge a = 0$ 
 $ml\_tl = \text{red} \vee il\_tl = \text{red}$ 
 $ml\_pass \in \{0, 1\}$ 
 $il\_pass \in \{0, 1\}$ 
 $ml\_tl = \text{red} \Rightarrow ml\_pass = 1$ 
 $il\_tl = \text{red} \Rightarrow il\_pass = 1$ 
 $a + b < d \wedge c = 0$ 
 $\vee c > 0$ 
 $\vee a > 0$ 
 $\vee b > 0 \wedge a = 0$ 
 $\vdash$ 
     $ml\_tl = \text{red} \wedge a + b < d \wedge c = 0 \wedge il\_pass = 1$ 
 $\vee il\_tl = \text{red} \wedge b > 0 \wedge a = 0 \wedge ml\_pass = 1$ 
 $\vee ml\_tl = \text{green}$ 
 $\vee il\_tl = \text{green}$ 
 $\vee a > 0$ 
 $\vee c > 0$ 

```



Ex.1

Study

Ex.2

```

 $d \in \mathbb{N}$ 
 $d > 0$ 
 $b \in \mathbb{N}$ 
 $ml\_tl = \text{red}$ 
 $il\_tl = \text{red}$ 
 $ml\_tl = \text{red} \Rightarrow ml\_pass = 1$ 
 $il\_tl = \text{red} \Rightarrow il\_pass = 1$ 
 $\vdash$ 
     $b < d \wedge ml\_pass = 1 \wedge il\_pass = 1$ 
 $\vee b > 0 \wedge ml\_pass = 1 \wedge il\_pass = 1$ 

```

Ex.3

```

 $d \in \mathbb{N}$ 
 $d > 0$ 
 $b \in \mathbb{N}$ 
 $ml\_tl = \text{red}$ 
 $il\_tl = \text{red}$ 
 $ml\_pass = 1$ 
 $il\_pass = 1$ 
 $\vdash$ 
     $b < d \wedge ml\_pass = 1 \wedge il\_pass = 1$ 
 $\vee b > 0 \wedge ml\_pass = 1 \wedge il\_pass = 1$ 

```

ARI

OR.L

OR.R2

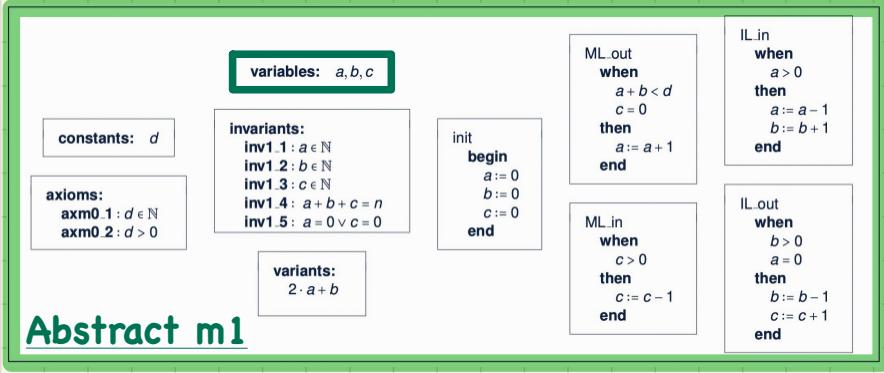
HYP

EQ.LR,MON

OR.R1

HYP

1st Refinement and 2nd Refinement: Provably Correct



Correctness Criteria:

- + Guard Strengthening
- + Invariant Establishment
- + Invariant Preservation
- + Convergence
- + Relative Deadlock Freedom

